

## Embedded Computer Architecture

2016-2017 Colleges: Tuesday 9,10 (Aud 7) and Thursday 7,8 (Aud 2)

Teacher: Henk Corporaal

## 5SIA0

Date	Topic	Material	Assignments + Remarks
15 Nov	Introduction	Ch 1	
17 Nov	Technology Impact	Ch 2	
22 Nov	<b>CGRA and Accelerators: Mark Wijtvliet</b>		<b>CGRA assignment (deadline: Dec 8)</b>
24 Nov	Processor Architectures - 1	Ch 3	
29 Nov	Processor Architectures - 2	Ch 3	
1 Dec	<b>GPU: Zhenyu Ye / Gert-Jan</b>		<b>GPU lab (deadline: Jan 9)</b>
6 Dec	Processor Architectures - 3	Ch 3; ARM	
8 Dec	Processor Architectures - 4	Ch 3	
13 Dec	Memory hierarchy	Ch 4	
15 Dec	Loop transformation for Data Reuse		(extended in compiler course)
20 Dec	Multiprocessor systems	Ch 5	
22 Dec	<b>GEM5+ Simulation: Mohammad Tahghighi + Luc Waeijen</b>	Ch 9	<b>GEM5 multiproc. assign. (deadline: Jan 22)</b>
10 Jan	Interconnection networks	Ch 6	
12 Jan	Coherence, synchr. and consistency	Ch 7	
17 Jan	<b>Deep Learning Neural Networks: Maurice Peemen</b>		
19 Jan	SMT: Simultaneous Multi-Threading + Wrap-up	Ch 8	