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Valentin Stefan GHEORGHITA

Personal details

Born: 25th March 1979, Ploiesti, Romania

Citizenship: Romanian

Education

- 2003 - 2007 PhD, Eindhoven University of Technology, Netherlands
- Faculty of Electrical Engineering, Information and Communication Systems Group
 - Thesis: *Dealing with Dynamism in Embedded Systems Design: Application Scenarios* (Online version @ TU/e library: <http://alexandria.tue.nl/extra2/200711726.pdf>)
- 2002 – 2003 Postgraduate studies, “Politehnica” University of Bucharest, Romania
- Computer Science and Engineering Department
 - Specialization: Advanced Systems for Internet Applications
 - GPA:10 / Graduation Thesis:10 (on a 1-10 scale)
 - Thesis: The Art of Translating Sugar to an Automata Language
- 1997 – 2002 Diplomat Software Engineer, “Politehnica” University of Bucharest, Romania
- Computer Science and Engineering Department (5 years)
 - Specialization: Basic Application Software Systems
 - GPA:9.44 / Graduation Thesis:10 (on a 1-10 scale)
 - Thesis: A Simulation Framework for Adaptive EPIC Processors

Professional experience

- Since 10/2007 Software Engineer – Google Zurich
- 09/2005–11/2005 Software Engineer (Intern) – Google Inc., Mountain View, USA
- Google Book Search Project: Design and optimization of (image processing) algorithms
- 04/2003–08/2003 Project Leader - NoBug Consulting, Bucharest, Romania
- Leded the design, implementation and testing of a translator from a properties specification language (PSL) to verification languages with support for specifying state machines (e.g. AXE, Verilog)
- 12/2002–03/2003 Software Engineer - UNIDEC Computer Systems Romania, Romania
NEC – Romanian Development Center, Computer Telephony and Unified Messaging Division
- Designed and implemented the following projects: Pitch and Speed Adjusting Plug-in for PCM 16bits Wave Sound, Kinesis PBX Integration
- 02/1999–06/2003 Teaching Assistant, “Politehnica” University of Bucharest, Romania
- Operating Systems Design (Spring 2003)
 - Operating Systems (Fall 2001, Fall 2002)
 - Microprocessor Based System Design (Fall 2001)
 - Computer Programming – The C Programming Language (Fall 1999, Fall 2000)
- 10/2000–11/2002 System Designer & Software Developer, “Prominic.ro” Bucharest, Romania
- Designed and developed client/server applications for a complete automation of the technical and billing processes for the internet service provider (Prominic.NET)
 - Configured and administrated LINUX, Solaris and AIX servers
- 10/2000–07/2003 Instructor, OAO RoEduNet Cisco Networking Academy, Bucharest, Romania
- 11/1998–02/1999 Computer Programmer, “Datek Telecom” Bucharest, Romania
- Designed and implemented a web-based e-mail application
 - Company web site
- Summer 1998 Computer Programmer, “Diamant S.A.”, Ploiești, Romania
- Programmed small applications for Intranet
- 1994 – 1997 Linux System administrator, ‘I.L.Caragiale’ High School, Ploiești, Romania
- Installed and administrated one of the first Romanian educational internet nodes

Technical Qualifications

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|-----------------|--|
| 11/2005 | Industrial Logic Inc. <i>Refactoring to Design Patterns Course</i> |
| 12/2000 | IBM E-business Academy <i>AD65 – IBM WebShere Application Server Course</i> |
| 03/1999–07/2000 | Cisco Networking Academy <i>Instructor course for Cisco Certificate Network Associate</i> |

Awards and scholarships

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|-----------------|---|
| 11/2006 | Best Paper Award at the IEEE Int. Symposium on System-on-Chip, Tampere, Finland. |
| 02/2002–08/2002 | Research training at the Department of Computer Science, National University of Singapore <ul style="list-style-type: none">Research activities in Adaptive Explicitly Parallel Instruction ComputingTwo published conference papers |
| 03/2000–09/2000 | CIMO/IAESTE training at Tampere University of Technology, Finland <ul style="list-style-type: none">Research activities in MPEG-4 transmission using RTP protocol |
| 03/1998–09/2000 | Scholarship of Excellence, “Politehnica” University of Bucharest, Romania <ul style="list-style-type: none">The university grants this scholarship each year to about 10 students (out of 15.000) |
| 02/1999 | 11 th Place in the 1998/1999 ACM Programming Contest World Finals (Netherlands) <ul style="list-style-type: none">Each university in the world is represented by one/two teams consisting in three participants |
| 02/1998 | 10 th Place in the 1997/1998 ACM Programming Contest World Finals (USA) |
| High-school | 3 Gold medals and one silver medal at International Olympiads in Informatics <ul style="list-style-type: none">At these contests each country is represented by four national level selected high-school students |

Special invitations and professional memberships

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|-----------------|---|
| 09/2004-09/2006 | Chairman of the PhD candidates organization from TU/e (approx. 300 members) <ul style="list-style-type: none">Represent the PhD candidates interests in front of the university entitiesPolicy makingIdentify and solve individual and group problems |
| Since 02/2002 | IEEE Member |
| 1997-1999 | Member of the jury at different high-school national contests in informatics Trainer of the Romanian national team for international contests |

Technical Skills

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|-----------------------|--|
| Fields of interest | Compilers, Embedded Systems, Operating Systems, Parallel and Distributed Systems |
| Programming Languages | C/C++, Java, x86 Assembly Language, Flex/Bison, SQL |
| Operating systems | Win32, Linux, Solaris, HP-UX |

Others

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| Languages | Romanian – mother tongue, English – fluent, Dutch – basic (read) |
| Hobbies | Travelling, hiking, history, squash, movies |

References are available upon request.

Working permit not required in Netherlands.

Selected Publications (full list at <http://www.ics.ele.tue.nl/~vali/publications.htm>)

- 07/2007 Scenario Selection and Prediction for DVS-Aware Scheduling (S.V. Gheorghita et al.), Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology © Springer.
- 11/2006 Application Scenarios in Streaming-Oriented Embedded System Design (S.V. Gheorghita et al.), in Proc. of the Int. Symposium on System-on-Chip (SoC 2006), pp. 175-178 © IEEE, *Best Paper Award*.
- 07/2006 Profiling Driven Scenario Detection and Prediction for Multimedia Applications (S.V. Gheorghita et al.), in Proc. of the Int. Conf. on Embedded Computer Systems: Architectures, Modeling, and Simulation (SAMOS VI), pp. 63-70 © IEEE.
- 07/2006 A Scenario-Aware Data Flow Model for Combined Long-Run Average and Worst-Case Performance Analysis (B.D. Theelen et al.), in Proc. of the 4th ACM-IEEE Int. Conf. on Formal Methods and Models for Codesign (MEMOCODE), pp. 185-194 © IEEE.
- 06/2006 Iterative Compilation for Energy Reduction (S.V. Gheorghita et al.), Journal of Embedded Computing, 1(4), pp. 509-520 © IOS Press.
- 09/2005 Intra-task Scenario-aware Voltage Scheduling (S.V. Gheorghita et al.), in Proc. of the Int. Conf. on Compilers, Architecture, and Synthesis for Embedded Systems (CASES 2005), pp. 177-184 © ACM.
- 06/2005 Automatic Scenario Detection for Improved WCET Estimation (S.V. Gheorghita et al.), in Proc. of 42nd Design Automation Conf. (DAC 42), pp. 101-104 © ACM.
- 05/2005 Constructing Checkers from PSL Properties (S.V. Gheorghita, R. Grigore), in Proc. of the 15th Int. Conf. on Control Systems and Computer Science (CSCS15), pp. 757-762.
- 03/2005 Dataflow Analysis for Real-Time Embedded Multiprocessor System Design (M. Bekooij et al.). Chapter 4 in Dynamic and Robust Streaming in and between Connected Consumer-Electronic Devices, pp. 81-108 © Springer.
- 12/2002 A Co-simulation Study of Adaptive EPIC Computing (V.S. Gheorghita, W.F. Wong, T. Mitra, and S. Talla), in Proc. of Int. Conf. of Field-Programmable (FPT 2002), pp. 268-275 © IEEE.
- 09/1999 A book: Information Technology (C. Cadar, V. Gheorghita), introductory high-school course in IT. (ISBN 973-98770-9-5). This book was counseled by the Ministry of Education as textbook for the 9th grade. More than 3000 books sold.