

# Modelleren en simuleren van de pipelined mini-mini MIPS processor

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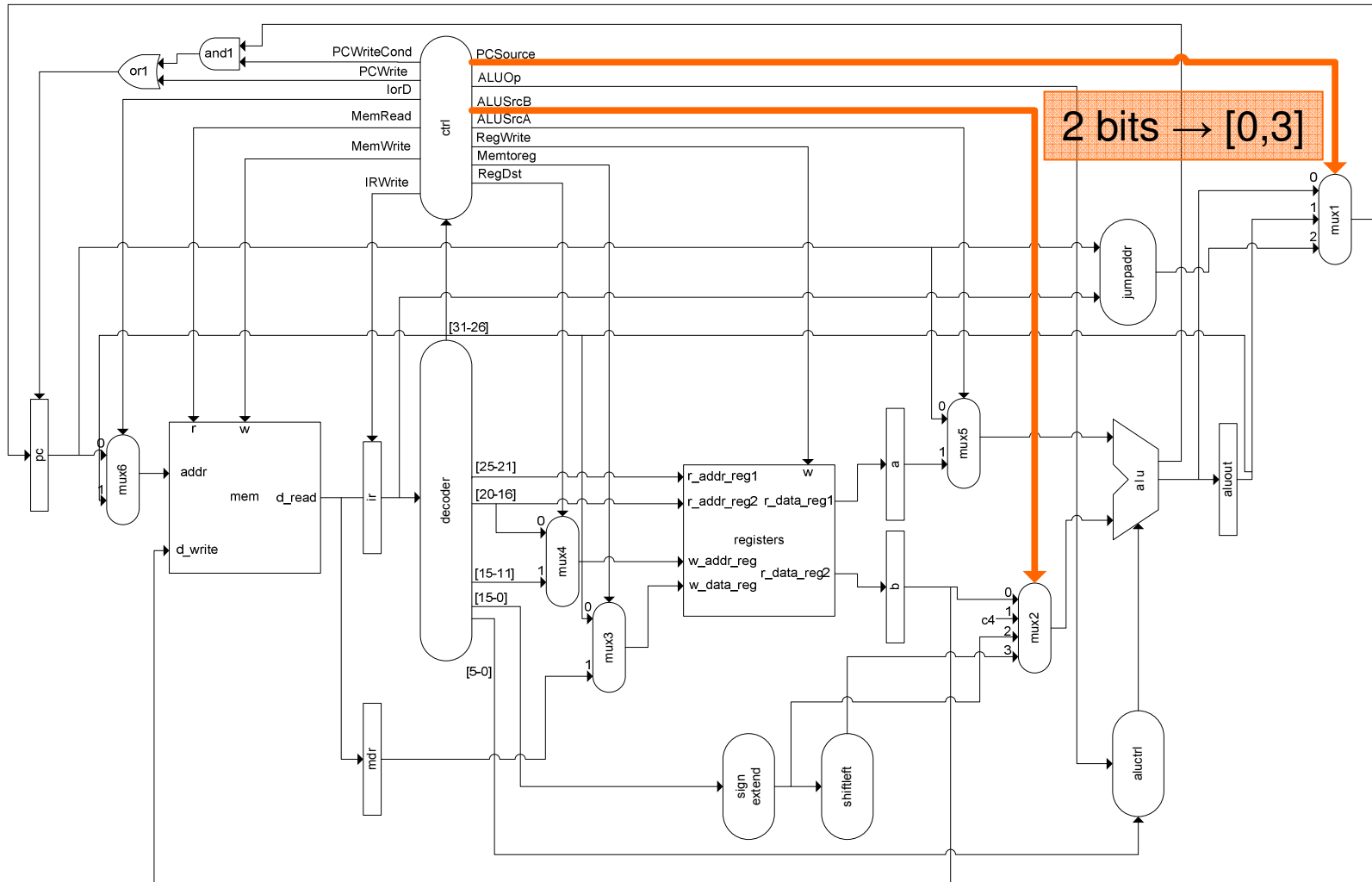


- **Mogelijkheid 1**

```
instr_t = instr.read();
instr_t = instr_t << 2;
instr_t = instr_t & 0x0FFFFFFF;
pc_t = pc.read();
pc_t = pc_t & 0xF0000000;
out_t = pc_t | instr_t;
out.write(out_t);
```

- **Mogelijkheid 2**

```
instr_t = instr.read();
instr_t = instr_t << 2;
pc_t = pc.read();
out_t = (pc_t.range(31,28), instr_t.range(27,0));
out.write(out_t);
```





# 7 Debuggen van de mMIPS

```
cygdrive/d/mmips/mips/multi_cycle_mmmips
$ cd /cygdrive/d/mmips/mips/multi_cycle_mmmips/
sstuijk@ICS-NB069 /cygdrive/d/mmips/mips/multi_cycle_mmmips
$ disas mips_mem.bin

mips_mem.bin:      file format binary

mips-objdump: mips_mem.bin: no symbols
Disassembly of section .data:

0000000000000000 <.data>:
0:  8c100040      lw      s0,64(zero)
1:  8c110040      lw      s1,68(zero)
8:  8c120040
c:  8c130040
10: 02328820
14: 0294a022
18: 02b5a822
1c: 12b30003
20: 0291a020
24: 02b0a820
28: 1022ffffc
2c: ac140050
30: 1000ffff
34: 00000000

40: 00000001
44: 0000001e
48: 00000046
4c: 00000005
50: ffffffff
```

