

Summary Premadona meeting

- Premadona meetings will be held every two weeks on Friday 14.00-15.00. Furthermore, Bart Theelen and Bart Mesman will have a meeting every week.
- A date and agenda for the official kick-off meeting have been set:

Date: October 15, 2004

Time: 15 - 17.30 h (including reception)

Location: TU/e - EE (precise location to be announced)

Proposed Agenda:

15.00 Overview	Henk Corporaal
15.30 Application track	Peter de With
15.40 Simulation track	Sander Stuijk
15.50 Network Architecture Layer	Jef van Meerbergen
16.00 Implementation and Demonstrator	Patrick Groeneveld
16.10 Design Flow	Bart Theelen
16.20 Resource Management and QoS	Jef van Meerbergen
16.30 Project Management	Bart Mesman
16.45 Discussion	
17.00 Reception	
17.30 Close	

The invitees are the involved people from TUE, Philips research, and the project partners: Gert-Jan van Dijk and Peter de With (LogicaCMG), Rene van de Berg (Philips Digital Car Radio, Business Line CarITS), and Rob Woudsma (Philips Business Unit Mobile Communications, BUMC). Location and reception will be organized by Bart Mesman (action point).

The agenda is organized in a manner similar to the flagship meeting from December last year: 6 topic presentations plus an overview plus project management. In this way the attendants can also see the embedding of Premadona in the broader cooperation TUE-Philips-LogicaCMG now called the flagship or NoC project. The presentations will be reviewed on oktober 8th at 13.00.

- Availability of Aethereal tools: These tools parameterize the network and its components and generate VHDL. We can have these tools available with regard to guaranteed throughput (and not best-effort) and network components. Jef van Meerbergen will try to obtain formal permission (action point). If we want to develop something ourselves, we at least aim at compliance at the service level. Jef will look for a service-level aethereal document and make it available (action point).
- The relation to Progress will be maintained via the official kick-off meeting, the user group meetings (every half year), the yearly Progress workshop, and possibly with Premadona workshops. Arno Moonen has submitted a paper/poster for the upcoming Progress workshop on his graduation work in Hijdra (Hapi models of

the communication network). Arno agreed to submit his work under the Premadona Umbrella (action point).

- For the vacancies in track 1 and 3, a number of applicants are available: Desingh and Akash currently do graduation work with Philips. Jef van Meerbergen and Bart Mesman will interview them (action point). Sadeque Atnam is a promising candidate mentioned by Peter de With for track 3. Peter will interview Sadeque (action point). Bart Mesman will post the Premadona advertisement on the academic research page of ASCII (action point).
- Application track: Peter de With has given a clear picture of the Mpeg4 main profile decoder that Milan Pastrnak is working on (on the shape-texture decoding part). This is planned as the main application case study for Premadona. Peter de With will look for some documentation on this application and make it available to Bart Theelen (action point). Potential accelerators in the planned design can be designed using the A|RT or Silicon Hive tools.
- There is no plan in Premadona to research debugging. In the Demons project at ICS, this issue is investigated. Bart has asked Calin to present/position his work in one of the upcoming Premadona meetings. Calin has gladly accepted, since it is a good opportunity for him to practice his presentation for the HLDTV conference. We still have to find a date, since the two planned meetings are already overloaded.
- There was no time to discuss in detail the design flow proposal from Bart Mesman corresponding to track 2. Another meeting is planned on September 16th from 16.30-17.30. Furthermore, Bart Theelen and Bart Mesman will have some meetings to converge to a design-flow proposal (action point).