

Embedded Computer Architecture**5SIA0**

2015-2016 Colleges: Monday 3,4 (IPO 0.98) and Thursday 7,8 (Pav. B2)

Teacher: Henk Corporaal

Date	Topic	Material	Assignments	Remarks
9 Nov, Monday	Introduction	Ch 1		
12 Nov, Thursday	Technology Impact	Ch 2		
16 Nov	Multiprocessor Assignment: Luc Waeijen	Ch 9	OpenMP - Sniper SIM	
19 Nov	Processor Architectures - 1	Ch 3	(deadline: Dec 7)	
23 Nov	Processor Architectures - 2	Ch 3		
26 Nov	VLIW - Silicon Hive: Menno Lindwer			
30 Nov	Processor Architectures - 3	Ch 3; ARM	VLIW lab	
3 Dec	CGRA + Accelerators: Mark Wijtvliet		(deadline: Dec 21)	
7 Dec	Memory hierarchy	Ch 4		
10 Dec	Multiprocessor systems - 1	Ch 5		
14 Dec	GPU: Zhenyu Ye		GPU lab	
17 Dec	Multiprocessor systems - 2	Ch 5	(deadline: Jan 15)	
4 Jan	Interconnectin networks	Ch 6		
7 Jan	Coherence, synchr. and consistency	Ch 7		
11 Jan	Chip multiprocessors	Ch 8		Multi-Threading, TM
14 Jan	NN Maurice Peemen			